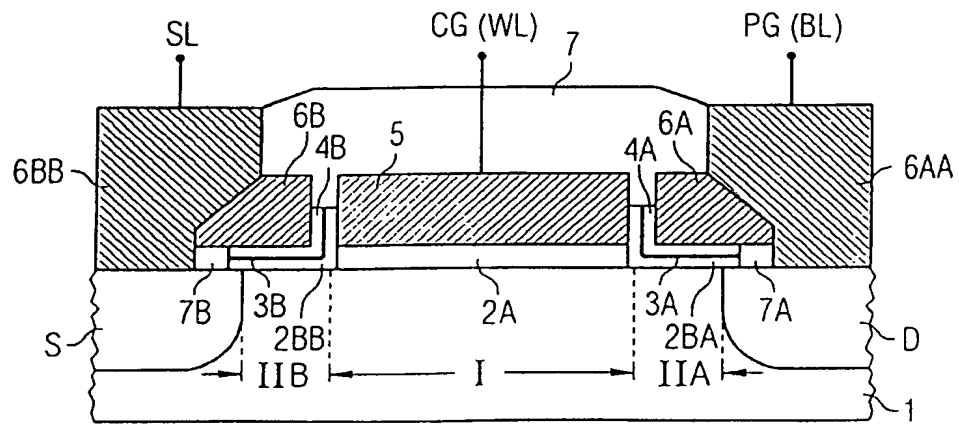


List of reference symbols

1	Semiconductor substrate
2, 2A	First insulating layer
3, 3A, 3B	Charge storage layer
2B, 2BA, 2BB	Second insulating layer
4, 4A, 4B	Third insulating layer
5	Control layer
6A, 6B	Programming layer
7, 7A, 7B	Fourth insulating layer
6AA, 6BB	Interconnect layer
S	Source region
D	Drain region
I	First part section
II, IIA, IIB	Second part section
SL	Source line
CG	Control electrode
PG	Programming electrode
WL	Word line
BL	Bitline

FIG 5



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FIG 1 Stand der Technik

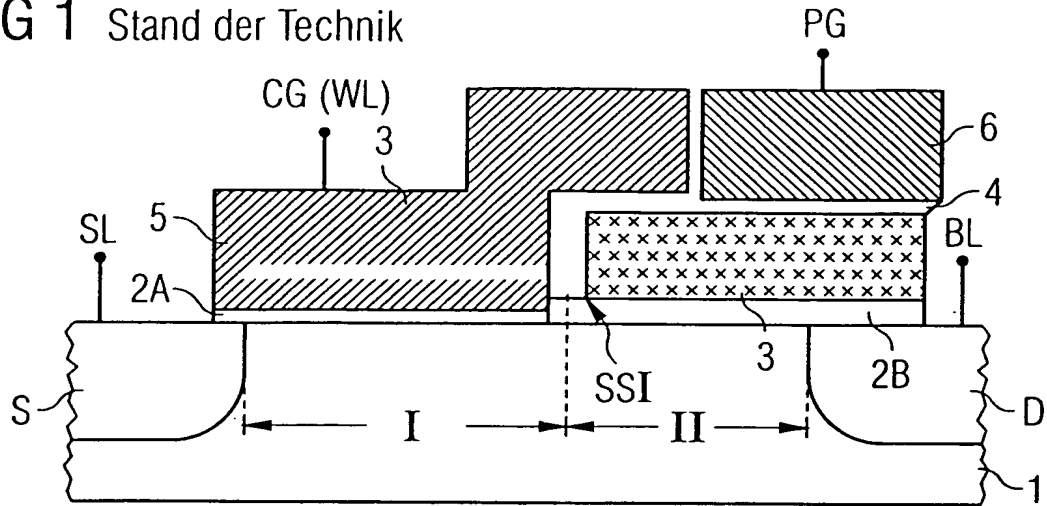


FIG 2 Stand der Technik

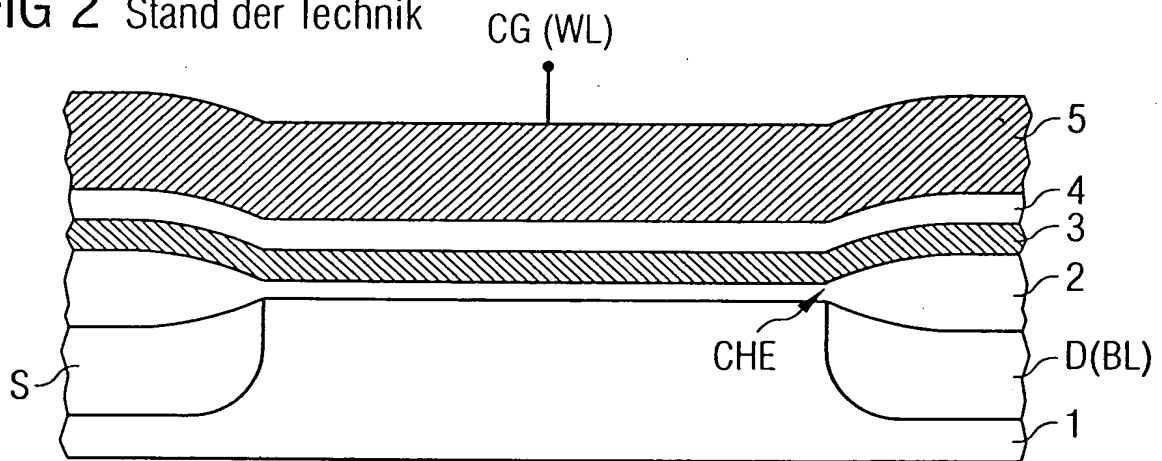
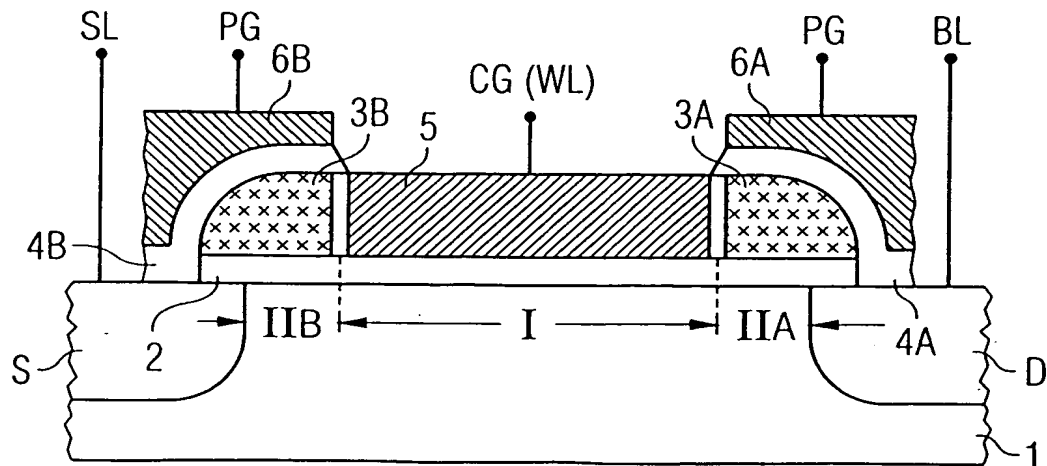
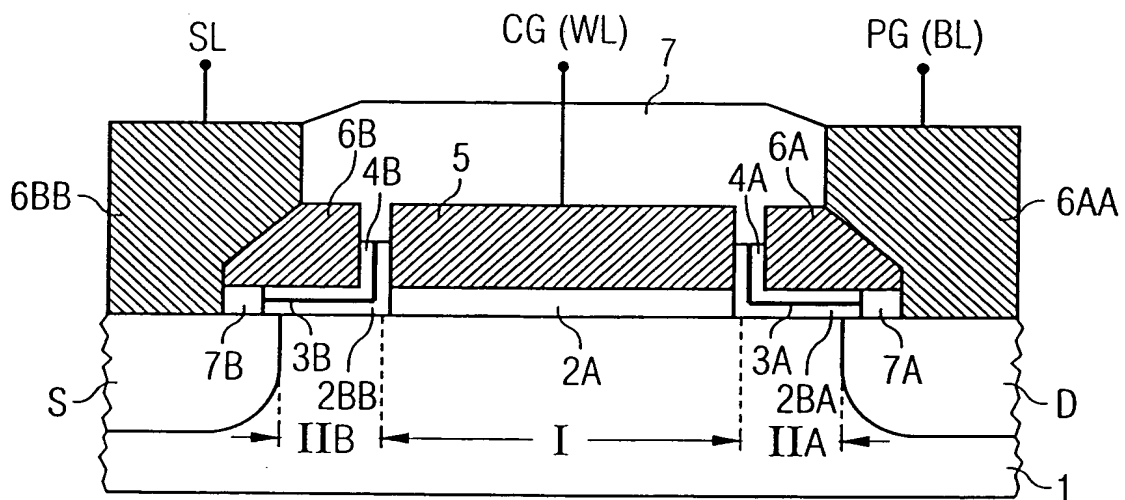


FIG 3 Stand der Technik





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FIG 6A

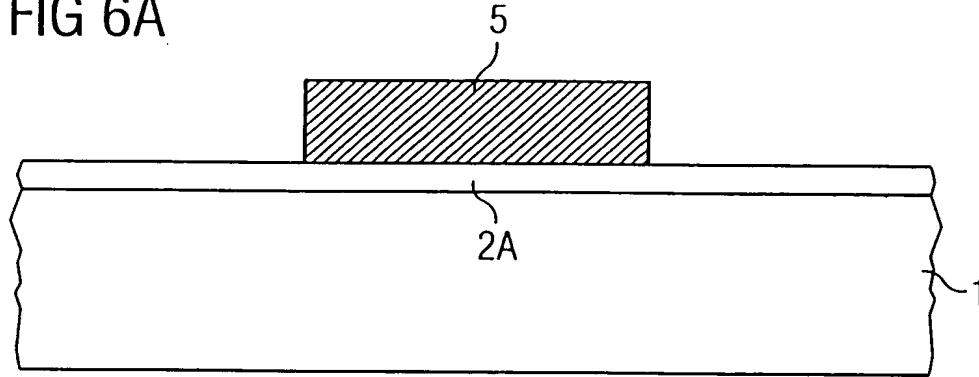


FIG 6B

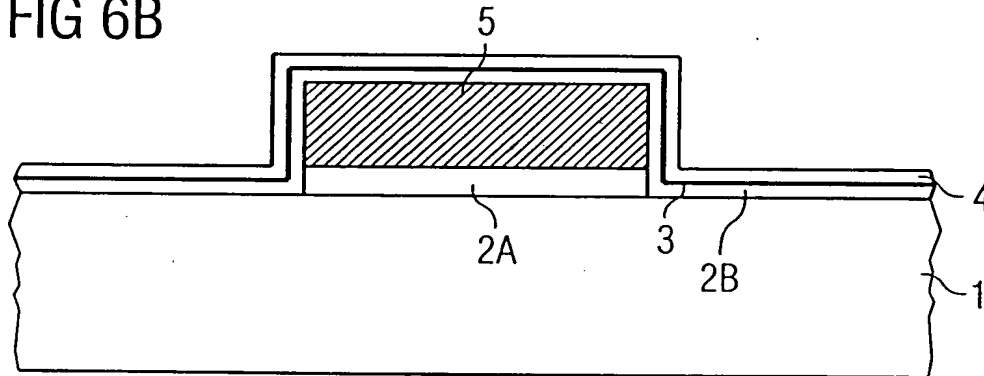
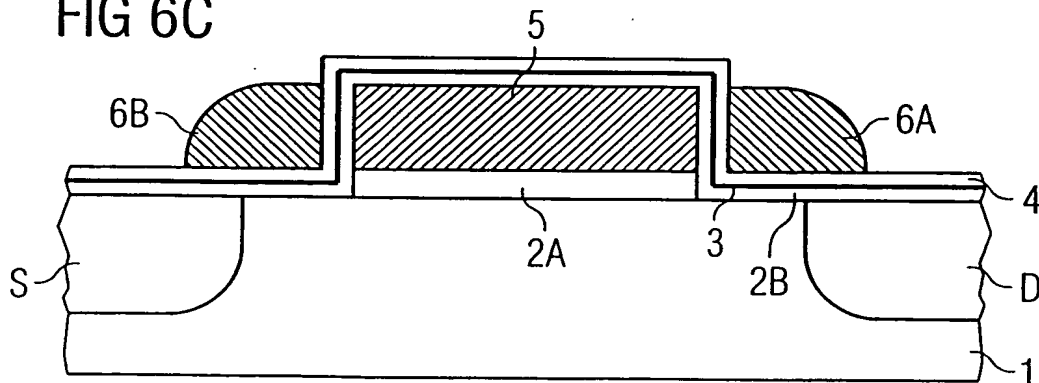


FIG 6C



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FIG 6D

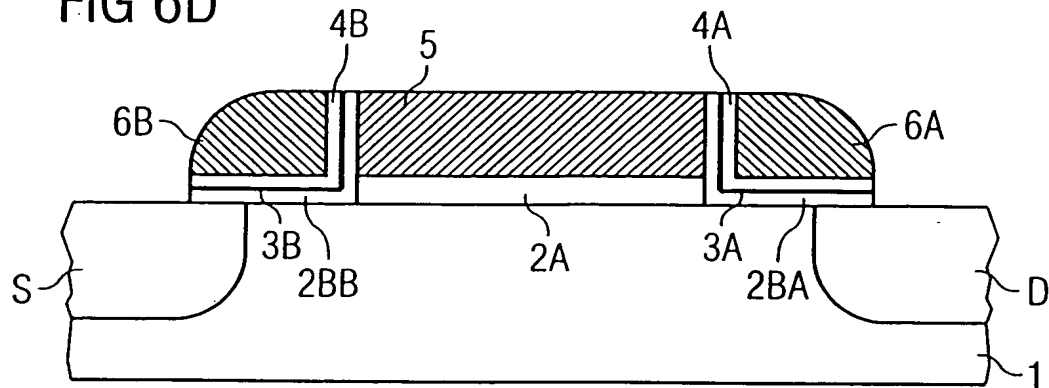


FIG 6E

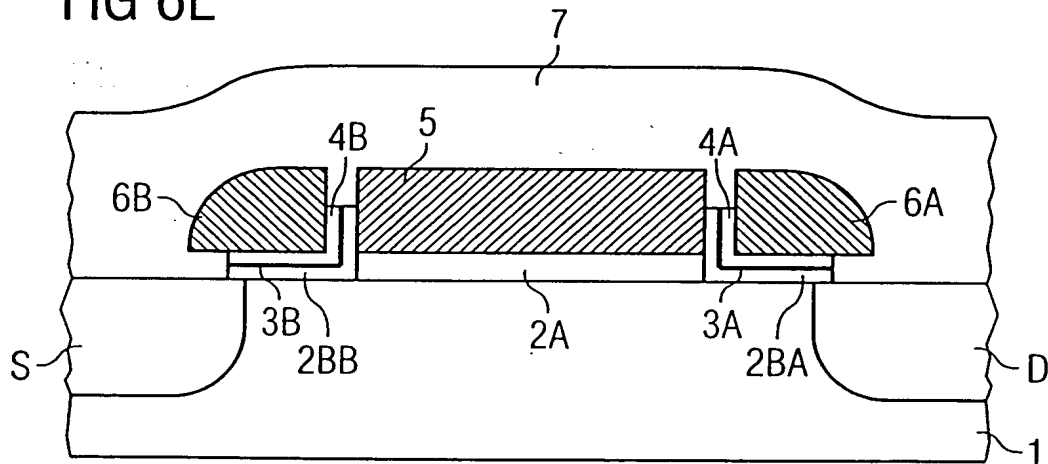


FIG 6F

